

FLASH ROM CONTENT UPDATING METHOD AND SYSTEM

BACKGROUND OF THE INVENTION

Field of the Invention

5 The invention relates to a flash ROM content updating method and a system implementing the same, and, more particularly, to a flash ROM content updating system using newly developed software to improve updating method.

Description of the Related Art

10 As new technologies rapidly progress, various types of digital devices and related components are developed with high integrity and performance. In addition to the desired lightweight and small size characteristics in hardware, multi-functional and user-friendly software device drivers and applications are also important for users to conveniently update devices themselves and solve hidden problems inside the devices
15 such as bugs.

With the application of information appliance (IA) products, it becomes increasingly demanding for memory types to improve and develop with more satisfying characteristics such as low power consumption, low cost that can adapt to and meet different requirements. As a result, various memories such as ferroelectric RAM
20 (FeRAM), magnetoresistive random access memory (MRAM) and ovonics unified memory (OUM) are currently available for specific market demands.

Back in 1981 when the PC was introduced, it was thought that 640K of the memory was sufficient to meet the requirement for PC memory at that time. However, it is obviously not sufficient for modern PDA devices, MP3 players, mobile phones and
25 other devices. It is predicted that a flash memory of over 100 MB will be required for a

mobile phone in the year of 2004. Beyond that, it is difficult to imagine how fast the capacity of a flash memory will grow in the future.

Presently, a digital product typically needs different types of memories including DRAM, SRAM, and flash, depending on the tasks required from the memory. For 5 example, flash memory is usually used to store BIOS settings. The SRAM and DRAM memory types are used in large amounts due to the demands of x86 architecture and the operating system controlling it. As digital products or information appliance (IA) products incorporate multi-media, communication functions, internet access, and data bases and other memory intensive tasks, a variety of erasable PROM devices have been 10 developed, such as UV-EPROM, EEPROM and flash ROM, so named depending on the way they update the information.

In a flash ROM, the information is accessed and written in block units. Although information processing in block units is faster than in bit or byte units, erasing and then writing whole blocks require a certain time interval. Furthermore, when there is an 15 interruption during information updating due to an unexpected cause such as an accidental power failure, the content of the ROM may be altered with potentially serious implications.

Furthermore, the whole File, for example, BIOS instructions usually has to be written to the flash ROM, even if only a portion of the File needs updating, which 20 results in increased processing time.

SUMMARY OF THE INVENTION

It is therefore an objective of the invention to provide a flash ROM content updating method and a system implementing the same method in which the information 25 stored in an electronic device such as a PDA or a PC can be quickly updated.

It is another objective of the invention to provide a flash ROM content updating method and a system implementing the same method in which information loss due to interruption of transmission during updating is reduced so as to increase the updating safety.

5 In the invention, check-sum of the updated information is performed after the updated information is divided into a plurality of blocks on the basis of the sizes of memory blocks of a primary information unit. The content of each update information block, including files, names, sizes, times, and dates, is given a binary check code after the check-sum processing. Then, logic operations and comparison of the check codes of 10 the primary memory blocks and the update information blocks are performed to determine whether the check codes for the update information blocks are consistent with the check codes for corresponding memory blocks. If they are, then the primary information stored in the memory blocks is not changed, otherwise it may have been altered due to virus attacks or other factors and needs updating. If the primary 15 information needs to be changed, then the update information is written to the memory blocks for all addresses corresponding to non-consistent check codes, that is where the check codes of the corresponding update and memory blocks are not the same.

The flash ROM content upgrading system according to the invention includes a primary information unit, an information register, and an updating device. The updating 20 device includes a control module, a logic comparison module, an information storage module, an addressing module, an information updating module, and a check-sum module. The primary information unit includes a plurality of memory blocks each of which stores a portion of the primary information. Check-sum of the primary information is performed to compute a plurality of check codes that are respectively 25 attached at the ends of the corresponding memory blocks. When updated information is received by the information register and the control module determines the input

information needs to be decoded, check-sum of the updated information is performed, and the updated information is divided into a plurality of update information blocks each of which has the same size as the corresponding memory block and further has a check code. When the check codes of the memory blocks are compared with those of the update information blocks, the address found are stored in the addressing module when the check codes of the corresponding updated and memory blocks are not consistent (non-consistent check codes). The information updating module then performs erasing and programming of the memory blocks based on the addresses stored in the addressing module.

To provide a further understanding of the invention, the following detailed description illustrates embodiments and examples of the invention, this detailed description being provided only for illustration of the invention.

BRIEF DESCRIPTION OF THE DRAWINGS

The present invention can be more fully understood by reading the following detailed description of the preferred embodiments, with reference made to the accompanying drawings wherein:

FIG. 1 is a block diagram of a flash ROM content upgrading system according to one embodiment of the invention;

FIG. 2 is a flow chart of flash ROM content updating according to one embodiment of the invention; and

FIG. 3A and FIG. 3B are schematic block diagrams showing the processing method for information stored in an information register and a primary information unit according to one embodiment of the invention.

FIG. 1 is a block diagram of a flash ROM content updating system according to one embodiment of the invention. As illustrated, the system of the invention includes an updating device 1, an information register 2, and a primary information unit 3. The updating device 1 receives input data from the information register 2 and then updates 5 the content of the primary information unit 3.

The information register 2 is, for example, a set of random access memories for temporary storage of access data and addresses contained in the device. The information register 2 stores the information to be updated received by the content updating system. The updated information then is divided into a plurality of update information blocks. In 10 this embodiment of the invention, the information register 2 is of a RAM type such as static RAM (SRAM) or dynamic RAM (DRAM), so the content of information register is lost when the power is turned off.

The primary information unit 3 is a main memory unit of an electronic device. The primary information unit 3 includes a plurality of memory blocks each of which stores 15 core system information such as BIOS settings, embedded software, or data files. In this embodiment of the invention, the primary information unit 3 is a flash ROM. The electronic device is, for example, a digital camera, an electronic dictionary, a personal digital assistant, a personal computer, or a laptop computer.

The updating device 1 further includes a control module 4, an information storage 20 module 5, a check-sum module 6, a logic comparison module 7, an addressing module 8 and an information updating module 9.

The check-sum module 6 performs the division into units of blocks and calculates 25 check-sums for both the blocks of data to be updated stored in the information register 2 and the blocks for the primary information stored in the primary information unit 3. The blocks of the data to be updated have the same size as the memory blocks of the primary information unit 3. The content of each update information block, including files, names,

sizes, times, and dates, is given a binary check code after check-sum processing. Each check code is attached at the end of the program block and retained in the information register 2 for comparison performed subsequently.

The logic comparison module 7 performs logic operations and comparison of 5 corresponding check codes for the memory blocks in the primary information unit 3 and the information register 2 to determine whether the check codes of each update information block are consistent with the check codes of each corresponding memory block. For example, as shown in FIG. 3A and FIG. 3B, a comparison is performed to determine whether a check-sum 1 code of one block to be updated in the information 10 register is consistent with a check-sum A code of its corresponding memory block.

The control module 4 is the operation core of the logic comparison module 7 and the other modules. In other words, the control module and the logic comparison module are the main processing modules implemented with either digital logic or a CPU running stored instructions. The control module 4 picks up an input command from the 15 information register 2 and decodes the command to perform the related functions (in this embodiment, an update function). Then the check-sum module 6 and the logic comparison module 7 subsequently perform the division into block units, calculation of the check-sums and the encoding and logic comparison of the updated data stored in the information register 2. The addressing module 8 picks up the addresses with respect to the non-consistent check codes of the corresponding updated information in the memory 20 blocks, as determined by the logic comparison module 7, as update addresses for updating. The addressing module 8 is a base address register or an instruction address register.

The information updating module 9 erases the preliminary information at a non- 25 consistent update address of the memory block in the primary information unit 3. Then, the information associated with this address in the information register 2 is written to

the corresponding update address in the primary information unit 3. The information updating module 9 is provided with function of ROM burner software.

The information storage module 5 has a large storage space for storage of primary programs, personal information, and the related applications and files of an electronic device.

FIG. 2 is a flow chart illustrating the flash ROM content updating process according to one embodiment of the invention.

In step S1, check-sum of core information such as BIOS or embedded software and data stored in a plurality of memory blocks in the primary information unit of a flash ROM is performed to compute a plurality of check codes respectively attached at the ends of the corresponding blocks. Then, step S2 is executed.

In step S2, information is received by the information register 2. Then, step S3 is executed.

In step S3, the control module 4 determines whether there is an update command. If no, the updating then is completed. If yes, step S4 is executed.

In step S4, the check-sum module 6 performs the division into block units of the information to be updated in the information register 2 on the basis of the size of the primary blocks. Then, the check-sum and encoding of the update information blocks are performed to respectively attach the check codes at the end of the corresponding blocks.

Then, step S5 is executed.

In step S5, the logic comparison module 7 performs logic operations and comparison of the check codes of the memory blocks respectively in the primary information unit 3 and the information register 2, and determines whether a check-sum 1 code of the update information block is consistent with a check-sum A sequence of the corresponding memory block. If yes, the updating then is completed. If no, step S6 is executed.

In step S6, the addressing module 8 picks up the address corresponding to the non-consistent check code of the updated and memory blocks as an update address for information updating.

5 In step S7, the information updating module 9 erases and then writes the content of the memory block at the update address. Then, step S8 is executed.

In the step S8, the information in the update memory block of the information register 2 is stored at the update address in the primary blocks of the primary information unit 1. Thereby, write processing of the updated information is performed. Then, the updating is completed.

10 FIG. 3A and FIG. 3B are schematic block diagrams illustrating the processing method for the information storage in the primary information unit 3 and the information register 2 according to one embodiment of the invention.

15 Referring to FIG. 3A, the information to be updated has been received by the information register 2 and undergoes division into block units that have check-sums attached by the check-sum module 6 to generate check-sum 1, check-sum 2, etc.

Referring to FIG. 3B, the core information stored in the primary information unit 3 of the electric device is processed to calculate and append check codes to the blocks by the check-sum module 6 to generate check-sum A, check-sum B, etc.

20 After the check-sum, encoding, and comparison of the information respectively stored in the primary information unit 3 and the information register 2, two situations are possible. Either check-sum 1 is equal to check-sum A, which means that the information in the block of check-sum A does not need updating. Or, check-sum 2 is not equal to check-sum B, which means the information in the primary information unit has changed or needs updating.

25 The invention has been described using exemplary preferred embodiments. However, it is to be understood that the scope of the invention is not limited to the

disclosed embodiments. On the contrary, it is intended to cover various modifications and similar arrangements. For example, the device type of the flash ROM according to the invention can be, but is not limited to, a flash ROM, or any other type of information storage memory device such as UV-EPROM, EEPROM, etc. The scope of the claims, therefore, should be accorded the broadest interpretation so as to encompass all such modifications and similar arrangements.